

3/24/04

INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)

ATTY DOCKET NO.

BUR920030123US1

SERIAL NO:

10/707,287

Gray et al.

FILING

12/03/2003

GROUP

2825

U.S. PATENT DOCUMENTS

*EXAMINER
INITIALPATENT & TRADEMARK OFFICE
FEB 2 0 2004

DOCUMENT NUMBER

DATE

NAME

CLASS

SUBCLASS

FILING DATE
IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

DOCUMENT NUMBER

DATE

COUNTRY

CLASS

SUBCLASS

TRANSLATION
YES NO

JP2001339312A

12/2001

Japan

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

"Flexible Image for Physical Layout of Circuits," A. R. Bruss et al., IBM Technical Disclosure Bulletin, Vol. 27, No. 11, April 1985, pp. 6687-6691.

"An Algorithm to Detect Positive Cycles in a Constraint Graph for Layout Compaction," K. Ishima et al., IEEE, CH2868-8/90/000-2853, 1990, pp. 2853-2856.

EXAMINER

DATE CONSIDERED

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)

Docket Number (Optional)

BUR920030123US1

Application Number

10/707,287

Applicant(s)

Gray et al.

Filing Date

12/03/2003

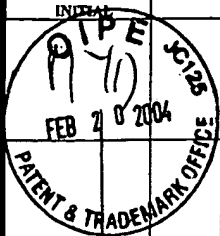
Group Art Unit

2825

*EXAMINER

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

INITIAL



"Process Independent Constraint Graph Compaction," D. Boyer, 29th ACM/IEEE Design Automation Conference, Paper 21.4, pp. 318-322.

"An Algorithm to Compact a VLSI Symbolic Layout with Mixed Constraints," Y. Z. Liao et al., 20th Design Automation Conference, Paper 9.1, IEEE, 1983, pp. 107-112.

"Graph-Optimization Techniques for IC Layout and Compaction," G. Kedem et al., 20th Design Automation Conference, Paper 9.2, IEEE, 1983, pp. 113-120.

"Symbolic Layout Compaction Review," D. Boyer, 25th ACM/IEEE Design Automation Conference, Paper 26.1, 1988, pp. 383-389.

"A Subjective Review of Compaction," Y. Cho, 22nd Design Automation Conference, Paper 25.1, IEEE, 1985, pp. 396-404.

"An Introduction to VLSI Physical Design," M. Sarrafzadeh et al., Chapter 7, "Compaction," Section 7.1.2, "Graph-based Techniques," McGraw-Hill (1996), pp. 276-279.

"Algorithms for VLSI Physical Design Automation," 3rd Edition, N. Sherwani, Chapter 12, "Compaction," Section 12.3.1, "Constraint-Graph based Compaction," Kluwer Academic Publishers (1999), pp. 453-463.

EXAMINER

[Handwritten signature]

DATE CONSIDERED

2/6/06

*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

12/03/03

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

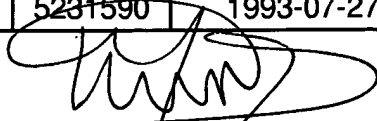
Electronic Version v18

Stylesheet Version v18.0

Title of Invention	CIRCUIT AREA MINIMIZATION USING SCALING						
<p>Application Number : 10/707,287</p> <p>Confirmation Number:</p> <p>First Named Applicant: Michael Gray</p> <p>Attorney Docket Number: BUR920030123US1</p> <p>Art Unit: 2825</p> <p>Examiner: MAGID DIMTAN</p> <p>Search string: (6587992 or 6576147 or 6526555 or 6507931 or 6434721 or 6412100 or 6412097 or 6378114 or 6295633 or 6216252 or 6202193 or 6189132 or 6099584 or 6044208 or 5974244 or 5936868 or 5889681 or 5689433 or 5663892 or 5636132 or 5625568 or 5619419 or 5598344 or 5477467 or 5416722 or 5231590).pn</p>							
US Patent Documents							
Note: Applicant is not required to submit a paper copy of cited US Patent Documents							
init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
M7D	1	6587992	2003-07-01	Marple			
	2	6576147	2003-06-10	Mukai			
	3	6526555	2003-02-25	Teig et al.			
	4	6507931	2003-01-14	Kotani et al.			
	5	6434721	2002-08-13	Chiluvuri et al.			
	6	6412100	2002-06-25	Sasagawa et al.			
	7	6412097	2002-06-25	Kikuchi et al.			
	8	6378114	2002-04-23	Shenoy et al.			
	9	6295633	2001-09-25	Murakawa			
	10	6216252	2001-04-10	Dangelo et al.			
	11	6202193	2001-03-13	Emura et al.			
	12	6189132	2001-02-13	Heng et al.			
	13	6099584	2000-08-08	Arnold et al.			
	14	6044208	2000-03-28	Papadopoulou et al.			
	15	5974244	1999-10-26	Hayashi et al.			
	16	5936868	1999-08-10	Hall			
	17	5889681	1999-03-30	Suda			
	18	5689433	1997-11-18	Edwards			
	19	5663892	1997-09-02	Hayashi et al.			
	20	5636132	1997-06-03	Kamdar			

11-10	21	5625568	1997-04-29	Edwards et al.			
	22	5619419	1997-04-08	D'Haeseleer et al.			
	23	5598344	1997-01-28	Dangelo et al.			
	24	5477467	1995-12-19	Rugg			
	25	5416722	1995-05-16	Edwards			
✓	26	5231590	1993-07-27	Kumar et al.			

Signature



02/06/06

Examiner Name	Date
HA 61P D/M/06	2/6/06